

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: Unassigned Filing Date: Herewith
Confirmation No.: Unassigned
First Named Inventor: Alexander Joffe
Assignee: Applied Micro Circuits Corporation
Examiner: Unassigned Art Unit: Unassigned
Attorney Docket No.: M-5648-2D US

San Jose, California
April 14, 2004

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are not enclosed as all documents were previously cited by or submitted to the Patent Office in prior applications relied upon for priority under 35 U.S.C. 120(37 C.F.R. § 1.98(d)). The prior applications are U.S. Application No. 09/055,033, filed April 3, 1998 (now Patent No. 6,330,584), and U.S. Patent Application No. 09/458,551, filed December 9, 1999.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
 2. a representation that a search has been made, other than as described above;
- or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

No fee is believed to be required. If a fee is required, please charge Deposit Account
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Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty. Docket No.		Serial No.		
				M-5648-2D US		Unassigned		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)				
(Use several sheets if necessary)				Alexander Joffe				
				Filing Date		Group		
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U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	2,686,844	Aug., 1954	Brewer.				
	AB	4,885,744	Dec., 1989	Lespagnol et al.				
	AC	5,058,144	Oct., 1991	Fiala et al.				
	AD	5,062,106	Oct., 1991	Yamazaki				
	AE	5,233,606	Aug., 1993	Pashan et al.				
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	AG	5,311,509	May., 1994	Heddes et al.				
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	AI	5,357,617	Oct., 1994	Davis et al.				
	AJ	5,361,337	Nov., 1994	Okin				
	AK	5,457,687	Oct., 1995	Newman				
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL	WO 95/20282	Jul., 1995	WO.				
	AM	WO 95/32570	Nov., 1995	WO.				
	AN	WO 98/09409	Mar., 1998	WO.				
	AO	JP 10049390	20 Feb. 1998	JP				
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	"An Overview of ATM LAN Emulation", World Wide Web, Interphase Corporation, Mar. 1996, pp. 1-8.						
	AR	"LAN Emulation", World Wide Web, Nov. 1995, pp. 1-18.						
	AS	B. Klessig, "ATM LAN Emulation", World Wide Web, 3Com Corporation, pp. 1-20.						
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AT	5,517,495	May., 1996	Lund et al.			
	AU	5,528,588	Jun., 1996	Bennett et al.			
	AV	5,541,912	Jul., 1996	Choudhury et al.			
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	BG	5,715,250	Feb., 1998	Watanabe			
	BH	5,719,853	Feb., 1998	Ikeda			
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	BK	5,809,024	Sep., 1998	Ferguson et al.			
	BL	5,838,968	Nov., 1998	Culbert			
	BM	5,983,004	Nov., 1999	Shaw et al.			
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	BO	Allyn Romanow et al. "Dynamics of TCP Traffic Over ATM Networks" IEEE Journal of Selected Areas in Communications, vol. 13, No. 4, pp 633-641, May 1995.					
	BP	Alan Demers et al. "Analysis and Simulation Of A Fair Queueing Algorithm" .COPYRG.T.1989 ACM, pp. 1-12.					
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	BQ	6,018,759	Jan., 2000	Doing et al.			
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	BT	"ATMS2003B Switch Controller 1 "White"", MMC Networks, Dec. 1995, 32 pages.					
	BU	Beraldi, R., et al: "Selective BECN Schemes for Congestion Control of ABR Traffic in ATM LAN", 1996 IEEE Int'l Conf. on Communications (ICC), Converging Technologies for Tomorrow's Applications, Dallas, Jun. 23-27, 1996. pp. 503-507.					
	BV	Dutton, Harry, J.R. and Lenhard, Peter, "Asynchronous Transfer Mode (ATM) Technical Overview" (IBM, Prentice-Hall PTR, New Jersey, 1995), pp. 3-1 through 3-25 and 9-1 through 9-34.					
	BW	Flanagan, William A., "ATM (Asynchronous Transfer Mode) User's Guide," (Flatiron Publishing, Inc. New York, 1994), pp. 25-49.					
	BX	Hongqing Li et al. "A Simulation Study of TCP Performance in ATM Networks With ABR and UBR Services" Proceedings vol. 3, 1996 IEEE Infocom '96, Fifteenth Annual Joint Conference of the IEEE Computer and Communications Societies, Mar. 24-28, 1996, pp v-xvi and pp 1269-1276 (21 pages total)					
	BY	Hongqing, Li, et al.: "Performance of TCP Over UBR Service in ATM Networks With Per-VC Early Packet Discard Schemes" Proceedings of the 1996 IEEE 15th Annual Int' Phoenix Conf. on Computers and Communication, Mar. 27-29, 1996, pp. 350-357.					
	BZ	Ozveren, C., et al: "Reliable and Efficient Hop-By-Hop Flow Control", Computer Communications Review, vol. 24, No. 4, Oct. 1, 1994, pp. 89-100.					
	CA	Tanenbaum, Andrew S., "Computer Networks" (Prentice Hall PTR, 3rd Ed., 1996) pp. 148-150.					
	CB	Farrens M. K. et al.: "Strategies For Achieving Improved Processor Throughput", Computer Architecture News, Association for Computing Machinery, New York, US, Vol. 19, No. 3 1 May 1991 (1991-05-01), Pages 362-369.					
	CC	Lee E. A. et al.: "Pipeline Interleaved Programmable DSP's: Architecture", IEEE Transactions on Acoustics, Speech and Signal Processing, IEEE Inc. New York, US, Vol. 35 No. 9, 1 September 1987 (1987-09-01), pages 1320-1333.					
	CD	Nemirovsky M.D. et al.: "Disc; Dynamic Instruction Stream Computer", Proceedings of the Annual+A44 International Symposium on Microarchitecture, XX, XX, 1991, pages 163-171.					
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